

ABSTRACT OF THE DISCLOSURE

A direct memory access system consists of a direct memory access controller establishing a direct
5 memory access data channel and including a first interface for coupling to a memory. A second interface is for coupling to a plurality of nodes. And a processor is coupled to the direct memory access controller and coupled to the second interface, wherein the processor
10 configures the direct memory access data channel to transfer data between a programmably selectable respective one or more of the plurality of nodes and the memory. In some embodiments, the plurality of nodes are a digital signal processor memory and a host processor
15 memory of a multi-media processor platform to be implemented in a wireless multi-media handheld telephone.

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